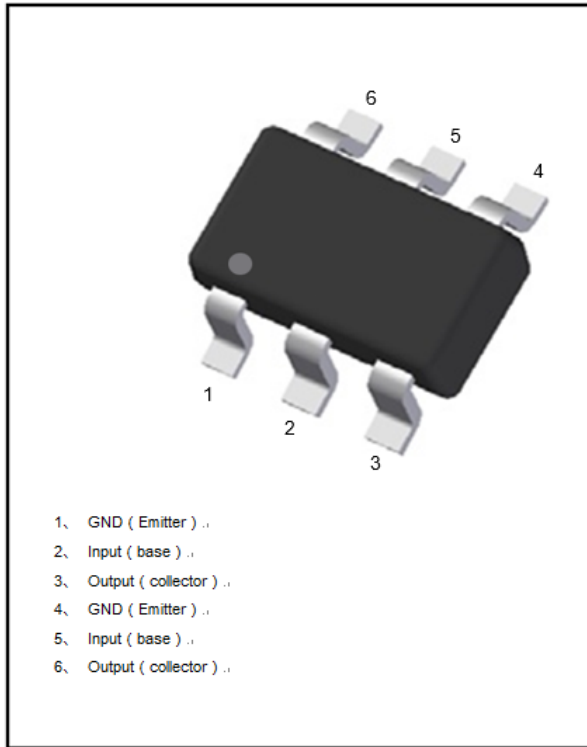


Dual NPN Digital Transistors (Built-in Resistors)

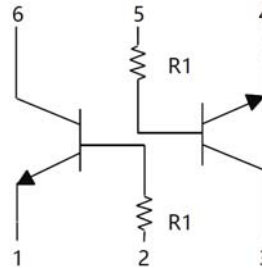


Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** H30
- **Equivalent circuit**



■Maximum Ratings (Ta=25°C Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Collector-Emitter Voltage	V_{CEO}	V		50
Collector-Base Voltage	V_{CBO}	V		50
Emitter-Base Voltage	V_{EBO}	V		5
Output Current	I_c	mA		100
Power Dissipation	P_D	mW		150
Junction Temperature (Single)	T_j	°C		150
Storage Temperature	T_{STG}	°C		-55 to +150



UMH30NS

■ Electrical Characteristics (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Collector-Base Breakdown Voltage	V_{CBO}	V	$I_C=50\mu A$	50		
Collector-Emitter Breakdown Voltage	V_{CEO}	V	$I_C=1mA$	50		
Emitter-Base Breakdown Voltage	V_{EBO}	V	$I_E=50\mu A$	5		
Collector Cut-off Current	I_{CBO}	μA	$V_{CB}=50V$			0.5
Emitter Cut-off Current	I_{EBO}	μA	$V_{EB}=4V$			0.5
DC current gain	h_{FE}		$V_{CE}=5V, I_C=20mA$	100		600
Input resistance	R_i	k Ω		1.54	2.2	2.86
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	V	$I_C=10mA, I_B=0.5mA$			0.3
Transition frequency	f_T	MHz	$V_{CE}=10V, I_E=5mA, f=100MHz$		250	

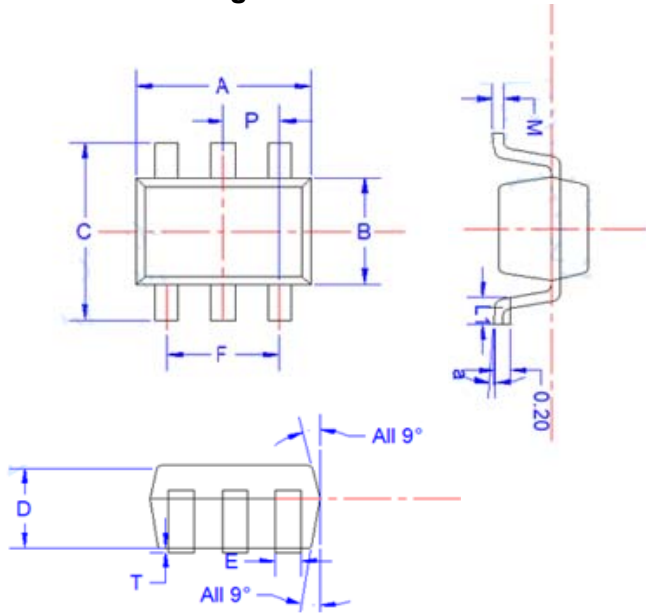
■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
UMH30NS	F2	Approximate 0.009g	3000	30000	120000	7" reel



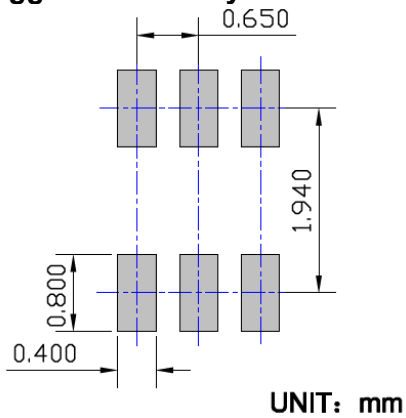
UMH30NS

■SOT-363S Package Outline Dimensions



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	0.15	0.25	0.35
B	1.15	1.25	1.35
C	2.00	2.10	2.20
P	0.650BSC		
A	1.80	2.00	2.20
T	0.00	0.05	0.100
D	0.90	0.95	1.00
L1	0.20	0.30	0.40
a	4°±4°		
M	0.10	0.15	0.25

■Suggested Pad Layout





Disclaimer

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